

**Part Number Cross Reference**

**FREQUENCY CONTROL SPECIFIC**

<b>AZM Part Number</b>	<b>Function</b>	<b>Packages</b>
AZ100EL16VO	ECL/PECL Oscillator Gain Stage and Buffer with Enable	T, U, L, N, XP, XR
AZ100LVEL16VR	ECL/PECL Oscillator Gain Stage & Buffer with Selectable Enable	L, N, XP, XR
AZ100LVEL16VT	ECL/PECL Oscillator Gain Stage & Buffer with Selectable Enable	L, N
AZ100LVEL16VV	Dual Frequency ECL/PECL Oscillator Gain Stage & Buffer with Enable	L
AZP81	PECL/ECL Filter-based Multiplier & Limiting Amp with Selectable Enable	L
AZP92	ECL/PECL $\div 1, \div 2$ Clock Generation Chip with Selectable Enable	N
AZP94	ECL/PECL $\div 1, \div 2$ Clock Generation Chip with Tristate Compatible Outputs	N
AZV99	PECL/LVDS Oscillator Gain Stage & Buffer with Selectable Enable	T, L, N, XP, XR
AZ12010	Multiply by 16, 32, Phase-Locked Loop Clock Generator	L

**Package Identifiers**

D = 8 Pin SOIC  
T = 8 Pin TSSOP (MSOP)  
U = 10 Pin TSSOP (MSOP)  
L = 16 Pin MLP (3x3 mm)  
N = 8 Pin MLP (2x2 mm)  
FN = 28 Pin PLCC  
XP = Die in Waffle Pack  
XR = Die on Tape & Reel



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**Part Number Cross Reference**

**LOGIC, 3-5V**

AZM Part Number	Function	Packages	Competition <sup>1</sup>
AZ100LVEL11	ECL/PECL 1:2 Differential Fanout Buffer	D, T, N	MC,SY
AZ100LVEL16	ECL/PECL Differential Receiver	D, T, N	MC,SY
AZ100LVEL32	ECL/PECL ÷ 2 Divider	D, T, N	MC,SY
AZ100LVEL33	ECL/PECL ÷ 4 Divider	D, T, N	MC,SY
AZ100LVEL58	ECL/PECL 2:1 Multiplexer	D, T, N	MC,SY

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**TRANSLATORS, BUS AND VIDEO DRIVERS, 3-5V**

AZM Part Number	Function	Packages	Competition <sup>1</sup>
AZ100ELT20	CMOS/TTL to Differential PECL Translator	D, T, N, XP, XR	MC, SY
AZ100ELT21	Differential PECL to CMOS/TTL Translator	D, T	MC, SY
AZ100ELT22	CMOS/TTL to Differential PECL Translator	D, T	MC, SY
AZ100ELT23	Dual Differential PECL to CMOS/TTL Translator	D, T	MC, SY
AZV99	PECL/LVDS Oscillator Gain Stage & Buffer with Selectable Enable	T, L, N, XP, XR	
AZP96	ECL/PECL Differential Receiver	N	
AZ10EL89	ECL/PECL Coaxial Cable Driver (5V ±10%)	D	MC, SY
AZG489	PECL Video Cable Driver (5V ±10%)	D	

1. MC = On Semiconductor, SY = Micrel Semiconductor



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## Part Number Cross Reference

### LOGIC LEGACY, 5V

AZM Part Number	Function	Packages	Competition <sup>1</sup>
AZ100EL01	ECL/PECL 4-input OR/NOR	D, T	MC, SY
AZ100EL07	ECL/PECL 2-input XOR/XNOR	D, T	MC, SY
AZ100EL11	ECL/PECL 1:2 Differential Fanout Buffer	D, T	MC, SY
AZ100EL16	ECL/PECL Differential Receiver	D, T	MC, SY
AZ100EL31	ECL/PECL D Flip Flop with Set and Reset	D, T	MC, SY
AZ100EL32	ECL/PECL $\div 2$ Divider	D, T	MC, SY
AZ100EL58	ECL/PECL 2:1 Multiplexer	D, T	MC, SY
AZ100E116	ECL/PECL Quint Differential Line Receiver	FN	MC, SY
AZ100E131	ECL/PECL 4-bit D Flip-Flop	FN	MC, SY
AZ100E142	ECL/PECL 9-bit Shift Register	FN	MC, SY
AZ100LVE210	ECL/PECL 1:4, 1:5 Differential Clock Driver (3-5V)	FN	MC, SY
AZ100LVE310	ECL/PECL 2:8 Differential Clock Driver (3-5V)	FN	MC, SY

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**SPECIALTY DEVICES, 3.3V**

<b>AZM Part Number</b>	<b>Function</b>	<b>Packages</b>
AZ100EL16VS	Differential Receiver with Variable Output Swing, 3.0-5.5V Supply	D, T
AZ100EP16	ECL/PECL Differential Receiver (3+GHz)	T
AZ100EP16VS	ECL/PECL Differential Receiver with Variable Output Swing (3+GHz)	T, L
AZ100EP16FE	ECL/PECL Differential Receiver with Variable Output Swing or Limiting Amplifier (4+GHz)	T

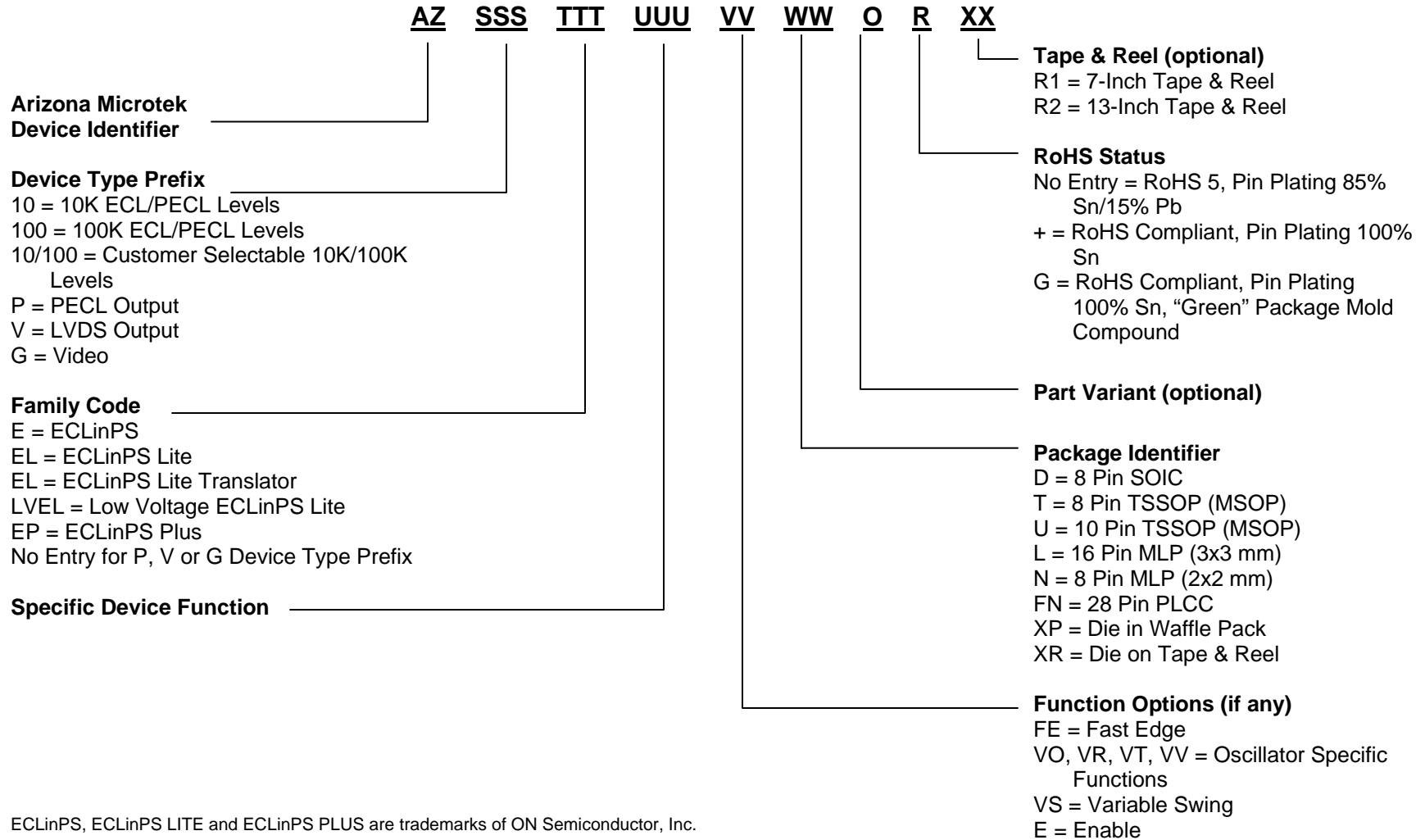
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Part Number Cross Reference

Part Number Identification



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